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PATENT

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G'Casey

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Sailesh Merchant, *et al.*

Serial No.: 09/092,158

Filed: June 5, 1998

For: METHOD FOR THE FABRICATION OF CONTACTS
IN AN INTEGRATED CIRCUIT DEVICE

Group No.: 2823

Examiner: Eaton, K.

Commissioner of Patents
and Trademarks
Washington, D. C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service
as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks,
Washington, D.C. 20231, on 5/30/2001 (Date)
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Sir:

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

In accordance with 37 C.F.R. §1.56, and in accordance with the provisions of 37 C.F.R. §§1.97 and 1.98 and §609 of the Manual of Patent Examining Procedure, Applicant hereby makes an additional disclosure of the patents, publications and other information listed below and on the accompanying form PTO-1449, which references are considered to be potentially material to the patentability of the invention disclosed in the above-referenced application. Copies of the listed references are submitted herewith. Disclosure has been filed previously on June 5, 1998.

U.S. Patent No.

Inventor

Date

5,688,718

Shue

November 18, 1997

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above patents and to question the relevance and materiality of the patents listed herein, in whole, in part or in combination, subsequent to filing this Information Disclosure Statement.

A check in the amount of \$ 180.00 is enclosed in payment of the filing fee for this communication. The Commissioner is hereby authorized to charge \$180.00 and any additional fees connected with this communication or credit any overpayment to Deposit Account No. 12-2325.

Respectfully submitted,

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